

Projects for course ES 330

Each project will incur 10% of your final grade

Project 1: Design an audio amplifier with speaker driver

Associated Labs: Lab 1 and Lab 2

Due Dates:

Project Phases	Date
Lab 1 Measurement Results	At the end of Lab 1
Lab 2 Measurement Results	At the end of Lab 2
Final Project Report	3rd Lab session (by midnight)

Project 2: Design a digital multimeter which will measure resistance and capacitance and generate digital output

Associated Labs: Lab 3 and Lab 4

Due Dates:

Project Phases	Date
Lab 3 Measurement Results	At the end of Lab 3
Lab 4 Measurement Results	At the end of Lab 4
Final Project Report	6th Lab session (by midnight)

Project 3: Design a 4 bit analog-to-digital and digital-to-analog converter

Associated Labs: Lab 5 and Lab 6

Due Dates:

Project Phases	Date
Lab 5 Measurement Results	At the end of Lab 5
Lab 6 Measurement Results	At the end of Lab 6
Final Project Report	9th Lab session (by midnight)

Project 4: Design a low-dropout voltage regulator

Associated Labs: Lab 7 and Lab 8

Due Dates:

Project Phases	Date
Lab 7 Measurement Results	At the end of Lab 7
Lab 8 Measurement Results	At the end of Lab 8
Final Project Report	12th Lab session (by midnight)